**Course name- Product Design Thinking and Framework**

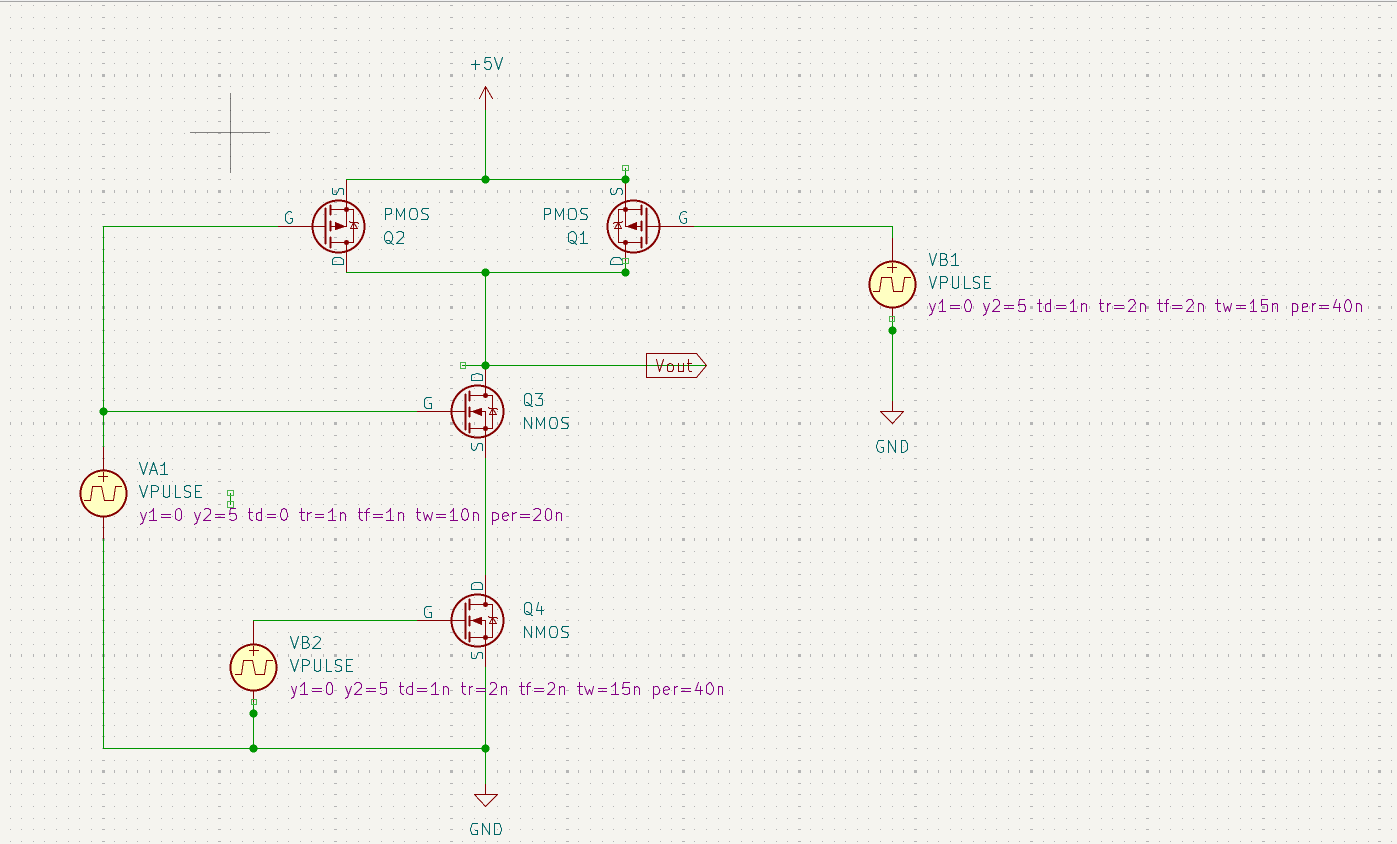
**Collage – Quantum University**

**Batch Number- 01**

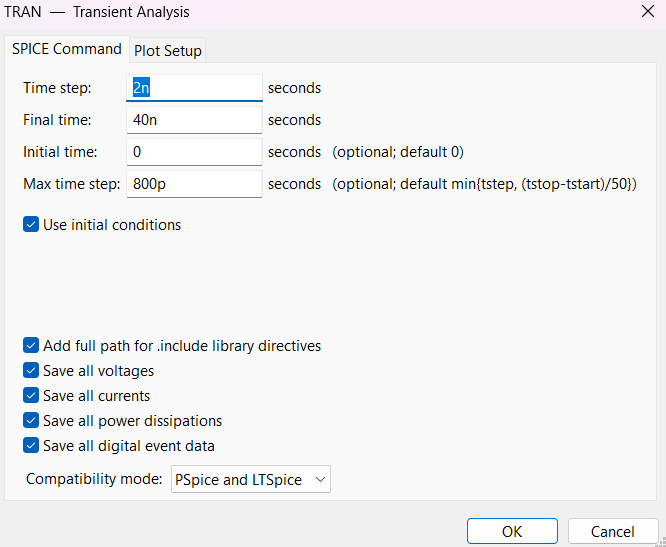
**Project Statement 4 , part (b)**

Construct and simulate a NAND gate using MOS transistors, infer its behaviour, and explain its truth table with logical operation.

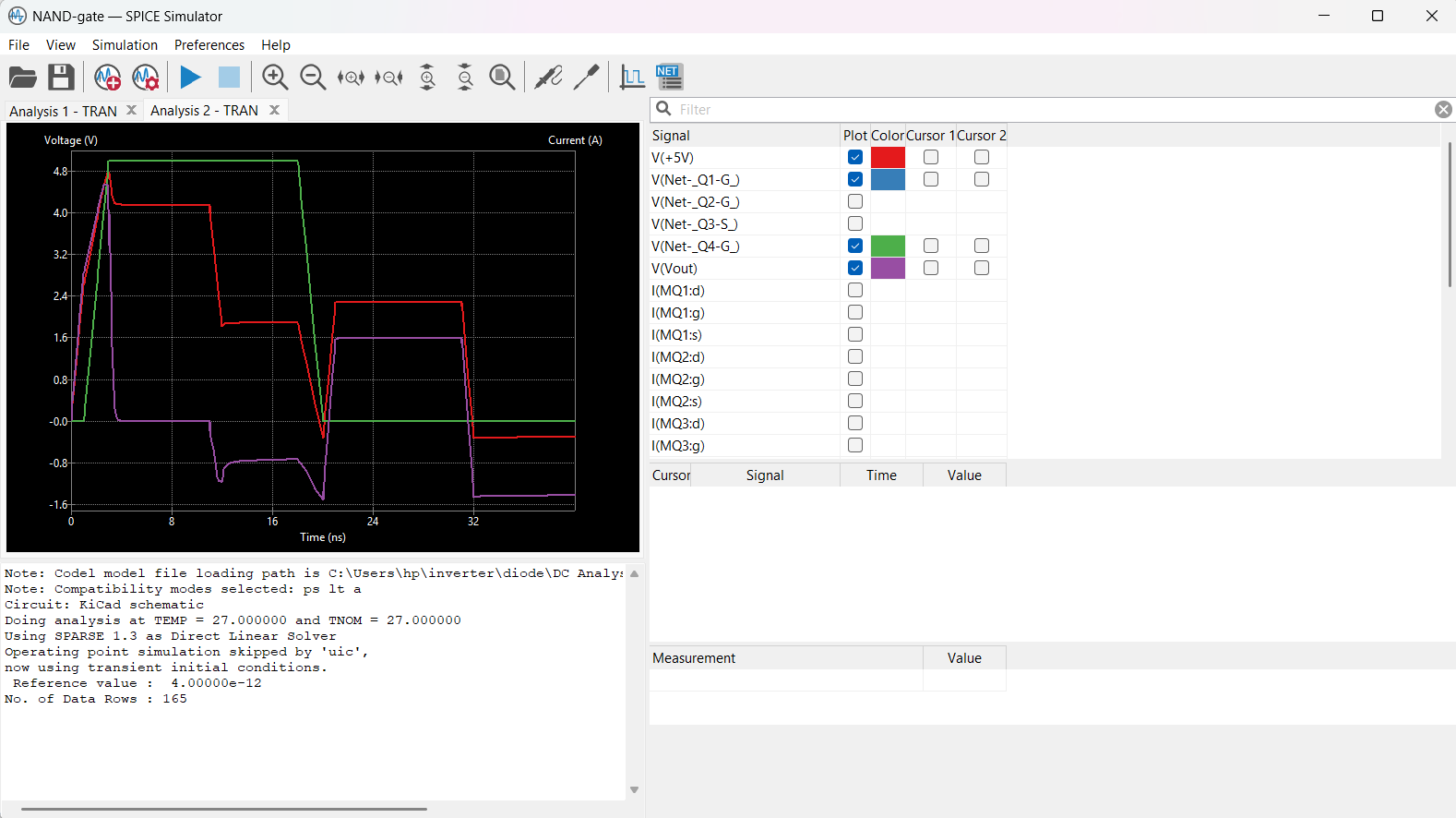
**Circuit:**

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**Values:**

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**Simulation:**

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